

Scott A. Cromar

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EDUCATION

University of Illinois College of Law, Champaign, IL

J.D., Expected May 2012

- University of Illinois Law Review, Internet Editor
- Honors in Legal Writing & Analysis
- Uhlenhop Endowed Scholarship (three-year, academic-based)
- Teaching Assistant for Legal Writing & Analysis, Fall 2010
- Intellectual Property Legal Society, Member

University of Illinois, Urbana-Champaign, IL

M.S., Electrical and Computer Engineering, 2009

GPA: 3.93/4.00

- IEEE Fortescue Graduate Scholarship (one-year, academic-based)
- Teaching Assistant and Research Assistant in the area of Electronic Design Automation
- Thesis: *High-Level Resource Binding and Allocation for Power and Performance Optimization*

University of California, Irvine, CA

B.S., Electrical Engineering, *magna cum laude*, 2007

GPA Overall: 3.85/4.00; GPA in Major: 3.99/4.00

- University of California Regents Scholarship (four-year, academic-based)
- Tau Beta Pi Engineering Honor Society

ADMISSIONS & TECHNICAL KNOWLEDGE

- United States Patent and Trademark Office, Registration No. 65,066
- Knowledge of: C, C++, Perl, PHP, HTML, Verilog, VHDL, Relational Databases, SQL, Semiconductor Devices & Manufacturing, Circuit Design, Nanotechnology, Microfluidics

EXPERIENCE

Knobbe Martens Olson & Bear LLP, Irvine, CA

Summer Associate, Summer 2011

Fitzpatrick, Cella, Harper & Scinto, Costa Mesa, CA

Summer Associate, Patent Prosecution, Summer 2010 & 2011

Jazz Semiconductor (Now TowerJazz), Newport Beach, CA

Engineering Intern, Device Modeling & Support, March–August 2007

University of California, Irvine, CA

Research Fellow, Nano Electronics and Spin Devices Laboratory, June–September 2006

Researcher, Integrated System Technology Solutions Laboratory, January–June 2006

RECENT PUBLICATIONS

- Scott Cromar, *Location of the Contemplated Sale: The Ultimate Guide in “Offer to Sell” Transnational U.S. Patent Infringement*, 2012 U. Ill. L. Rev. (forthcoming 2012).
- Deming Chen & Scott Cromar, *An Optimal Resource Binding Algorithm with Inter-Transition Switching Activities for Low Power*, J. Low Power Electronics (2009).
- Scott Cromar, Jaeho Lee & Deming Chen, *FPGA-Targeted High-Level Binding Algorithm for Power and Area Reduction with Glitch-Estimation*, Design Automation Conf. (2009).
- Gregory Lucas, Scott Cromar & Deming Chen, *FastYield: Variation-Aware, Layout-Driven Simultaneous Binding and Module Selection for Performance Yield Optimization*, Asia and S. Pac. Design Automation Conf. (2009). (**Best Paper Award**).

ACTIVITIES AND INTERESTS

LDS missionary (Albania), proficient Albanian, Eagle Scout, AIPLA, IEEE, waterskiing